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data in local storage. Flash Player Local Settings Manager - help.adobe.com - T11 103 U.S.C. 154(b) (b) by 470 days. day FOREIGN PATENT DOCUMENTS (21) Appl. No.: 13/070,186 JP 6-5094. A 1, 1994 JP 8-137744 A 5, 1996 (22) Filed: Mar. 23, 2011 JP 2002-108722. A 4, 2002 ... Field of Classification Search A data writing method for writing data to a flash memory USPC ..... 711/103: 714/773 includes writing an initial ... (54) DATA WRITING METHOD AND SYSTEM 2.93. E: 23. R hima.36 ... - At the start of FPGA configuration, this pin drives the SPI flash's MOSI pin and delivers a read instruction and the address. In x1 mode, this pin is output only. In x2 and x4 data width modes, this pin is bidirectional and receives data from the SPI flash. Using SPI Flash with 7 Series FPGAs Application Note (XAPP586) -

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